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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/739,737	12/20/2000	Tatsuhisa Nitta	P107254-00004	4987

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EXAMINER

NGUYEN, KIMNHUNG T

ART UNIT	PAPER NUMBER
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2674

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DATE MAILED: 05/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/739,737

Applicant(s)

NITTA ET AL.

Examiner

Kimnhung Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 13 January 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 11-15 is/are allowed.
- 6) ☒ Claim(s) 1-5, 8-10 and 16 is/are rejected.
- 7) ☒ Claim(s) 6-7 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

This Application has been examined. The claims 1-16 are pending. The examination results are as following.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Goddard (US patent 5,502,808) in view of Chang et al. (6,618,773) and in view Lvovsky et al. (US patent 5,159,683).

Goddard et al. disclose in figure 1 a display apparatus for displaying images based on signals received from a host (2) comprising determining means for determine an appropriate one of a plurality of interface types (Graphics processor, VGA hardware subsystem) to connect the display apparatus and the host; a plurality of storage means (3, 8). However, Goddard et al. do not disclose each storing specification information relating to the display for each one of the interface types to be connected, and an output means for switching between the plurality of storage means based upon the determination by determining means for outputting, from the storage means to the host, the specification information corresponding to the appropriate one of the interface types determined by

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said determining means. Chang et al. disclose in figures 1-3 a computer system (100) including each identification file (202, 204) identifies one of the display monitor's two interfaces (EDID 202, EDID 204) and the both are connected (see abstract, see column 6, lines 24-38). Lvovsky et al. disclose in figure 2, an automatic monitor sensing (38) graphic controller for use with a computer and a display monitor, the system having an outputs means for switching (see output of transistors 44, 48, see column 3, lines 44-52) between a computer and a display monitor (see column 2, lines 39-59), the controller (30) comprising a buffer (32) from the computer, and connected to 9-pin connector (36) of a display terminal or monitor, the signals or connections which are indicative of each type of monitor, e.g., monochromatic, conventional color...(see column 3, lines 4-12). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teaching of system having two distinct specifications EDID (202, 204) interfaces as taught by Chang et al. and an output means of transistors 44, 48 are stored from a computer and a plurality of signal lines as taught by Lvovsky et al. into the display apparatus having a plurality of storage means of Goddard et al. because this would be identified each interface as unique monitor with its own characteristics (see Chang et al., column 6, lines 26-30), and for the switching, it would be connected to both the signal lines and the display monitor and determined the capability of the display monitor (see Lvovsky et al. see abstract).

Regarding claim 9, Chang et al. disclose the specification information is EDID necessary for the Plug-and-play function (see abstract).

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3. Claims 2 and 4-5, 8, 10 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Goddard et al (US patent 5,502,808) in view of Chang et al. (6,618,773) and Lvovsky et al. (US patent 5,159,683) as applied as claim 1, and in view of the Title "Digital Visual Interface" cited by applicant).

Goddard et al. Chang et al. and Lvovsky et al. disclose a display apparatus in claim 1 as discussed above. Furthermore, Goddard et al. disclose VGA interface (see figures 1-2). Chang et al. disclose the specification information is EDID necessary for Plug-and Play function (see abstract) and a DDC (208a, Display Data Channel) (see figure 2, column 6, lines 1-5). However, Goddard et al., Lvovsky et al. and Chang et al. do not disclose a storage means for DVI-I interface, and a 5V line for DDC (Display Data Channel) which is opened or ground. The "Digital Visual Interface" discloses in pages 25 and 43 that DVI interface, and a 5V line for DDC (Display Data Channel) (see page 15). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teaching of the Title "Digital Visual Interface" discloses in pages 25 and 43 having DVI-I interface and a 5V line for DDC into the system display of Goddard et al., Chang et al. and Lvovsky et al. for producing the claimed invention because this would for providing a monitor with a digital interface to attach directly to either system connector.

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4. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Goddard et al. (US patent 5,502,808) and Chang et al. (US patent 6,618,773) in view of Lvovsky et al. (US patent 5,159,683) as applied claim 1 and in view of Gradinariu (US patent 6,378,008).

Goddard et al. Chang et al. and Lvovsky et al. disclose a display apparatus for displaying images based on signals received from a host as discussed in claim 1 above. However, Goddard et al. Chang et al. and Lvovsky et al. do not disclose that the output means comprises a multiplexer. Gradinariu discloses in figure 3 that the output comprises a multiplexer (495) which carries the data out off the memory circuit. It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teaching of Gradinariu in the system of Goddard et al., Chang et al. and Lvovsky et al. for producing the claimed invention because this would provide the output the logic state of a selected memory cell.

Allowable Subject Matter

5. Claims 11-15 are allowed.

6. Claims 6-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

7. The following is a statement of reasons for the indication of allowable subject matter:

None of the cited art do not disclose a display apparatus as define in claim 5, further comprising a peak hold circuit for generating a DC voltage based on synchronizing signals received from said host, said DC voltage generated by said peak hold circuit being supplied only to said storage

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means for the VGA interface and said multiplexer as claim 6, or a backflow preventing diode disposed between said peak hold circuit and storage means for the DVI-I interface as claim 7, or a multiplexer operably connected to the first and second storage memories, the multiplexer outputs DVI-I specification information to the graphics card in order for the display device to properly display images in accordance with the specification information for the DVI-I interface and when the graphics card voltage is determined to be absent, the multiplexer outputs the specification information for the VGA interface to the graphics card in order for the display device to properly display images in accordance with the VGA specification as claim 11, or the backflow preventing diode is disposed between said peak hold circuit and said storage means for the DVI-I interface for blocking a DC current flowing from said hold circuit as claim 15.

Response To Arguments

8. Applicant's arguments filed on 1-3-04 have been fully considered but they are not persuasive.

Applicant argues that the prior art does not disclose the storage means, which stores specification (EDID). However, examiner respectfully disagrees with the arguments because the combinations of Goddard and Chang et al. Goddard discloses the storage means, Chang et al. disclose the two types of specifications EDID 202 and 204 as discusses above. For these reasons, the rejections are maintained.

9. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number (703) 308-0425.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD A HJERPE** can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

Or faxed to:


(703) 872-9314 (for Technology Center 2600 only).

Hand-delivery response should be brought to: Crystal Park II, 2121 Crystal Drive, Arlington, VA Sixth Floor (Receptionist).

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Kimnhung Nguyen
May 24, 2004


RICHARD HJERPE 5/27/04
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600